

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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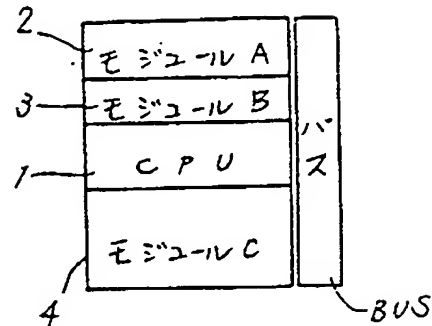
APPLICATION DATE : 26-09-86  
APPLICATION NUMBER : 61226010

APPLICANT : HITACHI MICRO COMPUT ENG LTD;

INVENTOR : KIHARA TOSHIMASA;

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TITLE : DESIGN SYSTEM FOR  
MICROCOMPUTER



ABSTRACT : PURPOSE: To develop a microcomputer conforming to the requirements of user in a short period by forming a microprocessor section, a peripheral circuit and a memory circuit as modules having nearly the same lateral width and connecting the modules by a bus.

CONSTITUTION: Plural types different in storage capacity are prepared for memory modules such as a mask ROM module, an EPROM module, an EEPROM module and a RAM module. Then the layout of each element and wiring is designed so that the lateral width of each module expanded into the circuit pattern is nearly constant. Then the wiring is applied so that input/output terminals of each circuit, that is, the entrance/exit of the signal are arranged on one side of each module at an equal interval or with regularity. Then the arrangement of the modules selected by the user is decided. In this case, the length of the wiring (bus) connecting the modules is designed as short as possible. Thus, the user can develop a microcomputer conforming to its requirements in a short period.

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